JAN 80 700 B

Application Information

Application Serial No.:: 10/512,405

Application Type:: Regular

Subject Matter:: Utility

Suggested Group Art Unit:: N/A

CD-ROM or CD-R?:: None

Sequence submission?:: None

Computer Readable Form (CRF)?:: No

Title:: HIGH-RESISTANCE SILICON WAFER

AND ITS MANUFACTURING METHOD

Attorney Docket Number:: 474082002800

Request for Early Publication?:: No

Request for Non-Publication?:: No

Total Drawing Sheets:: 6

Small Entity?:: No

Petition included?:: No

Secrecy Order in Parent Appl.?:: No

Applicant Information

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Japan

Status:: Full Capacity

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Family Name:: TAKASE

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Applicant Authority Type:: Inventor

Primary Citzenship Country: Japan

Status:: Full Capacity

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Primary Citizenship Country:: Japan

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Applicant Authority Type: Inventor

Page # 2

SUPPLEMENTAL 01/30/06

Status::

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Correspondence Information

Correspondence Customer Number:: 25227

Representative Information

Representative Customer Number:: 25227

Continuity Information

This application is a::

35 USC 371

Application No.::

PCT/JP03/04866

Filing Date::

April 16, 2003

Foreign Priority Information

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	2002-127509	April 26, 2002	Yes
Japan	2002-360731	December 12, 2002	Yes

Assignee Information

Assignee Name::

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